

PTO-1449	Application No.	Applicant(s) James D. Ballew et al.	
	Docket Number 064747.1015	Group Art Unit	Filing Date April 15, 2004

**Information Disclosure Citation  
in an Application**

**U.S. PATENT DOCUMENTS**

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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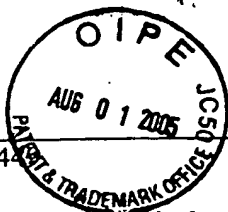
**NON-PATENT DOCUMENTS**

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
EM ↓ ↓ ↓ ↓	O	Patent Application entitled, "High Performance Computing System and Method", 47 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1012	04/15/2004
	P	Patent Application entitled, "System and Method for Cluster Management Based on HPC Architecture", 49 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1014	04/15/2004
	Q	Patent Application entitled, "Graphical User Interface for Managing HPC Clusters", 48 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1016	04/15/2004
	R	Patent Application entitled, "System and Method for Topology-aware Job Scheduling and Backfilling in an HPC Environment", 49 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1017	04/15/2004
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EXAMINER /Elmira Mehrmanesh/

DATE CONSIDERED 09/27/2006


EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.



PTO-142		Application No. 10/826,959		Applicant(s) James D. Ballew et al.			
Information Disclosure Citation In an Application		Docket Number 064747.1015	Group Art Unit	Filing Date April 15, 2004			
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							YES NO
<b>NON-PATENT DOCUMENTS</b>							
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EM	E	Communication from the European Patent Office, European Search Report for Application No. EP 05 25 2235, 3 pages, 07/22/2005, 3 pages					07/22/2005
	F	Rzymianowicz et al., "Clustering SMP Nodes with the ATOLL Network: A Look into the Future of System Area Networks," Proceedings of High Performance Computing, 8th International Conference, May 8, 2000, 10 pages					05/08/2000
	G	Bhanot et al., "The BlueGene/L Supercomputer," 20th International Symposium on Lattice Field Theory," vol. 119, June 2002, 8 pages					06/2002
	H	Advanced Micro Devices et al., "Hypertransport Technology I/O Link - A High-Bandwidth I/O Architecture," July 20, 2001, pp. 1-25					07/20/2001
	I	Pinkston et al., "InfiniBand: The "De Facto" Future Standard for System and Local Area Networks or Just a Scalable Replacement for PCI Buses?," Cluster Computing-Kluwer Academic Publishers, vol. 6, no. 2, 2003					2003
	J	Culler et al., "Parallel Computer Architecture - A Hardware/Software Approach, Interconnection Network Design," Morgan Kaufmann, 1999, 30 pages					1999
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DAL01:863945.1

PTO-1449		Application No. 10/826,959		Applicant(s) James D. Ballew et al.		
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EM	E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012031, 6 pages, 08/01/2005 and Written Opinion of the International Search Authority, 5 pages				08/01/2005
	F	Chang et al., "Performance Improvement of Allocation Schemes for Mesh-Connected Computers," Journal of Parallel and Distributed Computing, Academic Press, Duluth, MN, vol. 52, no. 1, July 10, 1998, 30 pages				07/10/1998
	G	Wanqian, et al. "Non-Contiguous Processor Allocation Algorithms for Distributed Memory Multicomputers," Supercomputing '94, Proceedings, Washington, D.C., November 14-18, 1994, 10 pages				11/14/1994 - 11/18/1994
	H	Krevat et al., "Job Scheduling for the BlueGene/L System," Lecture Notes in Computer Science, vol. 2537, July 24, 2002, 18 pages				07/24/2002
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<b>Information Disclosure Citation In an Application</b>		<b>Application No.</b> 10/826,959		<b>Applicant(s)</b> James D. Ballew et al.			
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EM	A	4,868,818	09/19/1989	Madan	371	11.3	10/29/1987
	B	5,020,059	05/28/1991	Gorin et al.	371	11.3	03/31/1989
	C	5,748,882	05/05/1998	Huang	395	184.01	05/08/1996
	D	5,926,619	07/20/1999	Badovinatz et al.	395	182.02	01/05/1998
	E	6,453,426	09/17/2002	Gamache et al.	714	4	03/26/1999
	F	6,629,266	09/30/2003	Harper et al.	714	38	11/17/1999
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V	H	2003/0097487	05/22/2003	Rietze et al.	709	325	11/20/2001
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							<b>YES</b> <b>NO</b>
EM	I	WO 03/005192	01/16/2003	PCT	G06F	9/445	X
EM	J	EP 0 981 089 A2	02/23/2000	European Patent Office	G06F	11/14	X
<b>NON-PATENT DOCUMENTS</b>							
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EM	K	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012313, 7 pages, 09/20/2005, and Written Opinion of the International Search Authority, 7 pages					09/20/2005
	L	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012316, 7 pages, 09/14/2005, and Written Opinion of the International Search Authority, 7 pages					09/14/2005
	M	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012242, 7 pages, 09/19/2005, and Written Opinion of the International Search Authority, 9 pages					09/19/2005
V	N	Davidson, U.S. Patent Application Serial Number 10/991,754, filed 11/17/2004, entitled "Fault Tolerance and Recovery in a High-Performance Computing (HPC) System, 117 pages					11/17/2004
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DAL01:874223.1



<b>Information Disclosure Citation In an Application</b>		<b>Application No.</b> 10/826,959		<b>Applicant(s)</b> James D. Ballew et al.			
		<b>Docket Number</b> 064747.1015	<b>Group Art Unit</b>	<b>Filing Date</b> April 15, 2004			
<b>U.S. PATENT DOCUMENTS</b>							
		<b>DOCUMENT NO.</b>	<b>DATE</b>	<b>NAME</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>FILING DATE</b>
EM	A	5,301,104	04/05/1994	Yalamanchili	395	800	08/07/1990
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EM	E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012314, 6 pages, 09/20/2005, and Written Opinion of the International Search Authority, 7 pages					Mailed 02/10/2006
	F	Feitelson, "Job Scheduling in Multiprogrammed Parallel Systems", IBM Research Report, August 1997, pages 1-4, XP002942107					August 1997
	G	Kandlur et al., "Hypercube Management in the Presence of Node Failures", third conference on hypercube concurrent computers and applications ACM New York, 1988, pages 328-336, vol. 1, XP002364194					1998
	H	Wenjian et al., "Efficient Processor Allocation for 3D Tori", Parallel Processing Symposium 1995, Proceedings, 9th International, April 25-28, 1995, IEEE Comput. Soc., pages 466-471, XP010135921					1995
	I	Ma et al., "E-Kernel: An Embedding Kernel on the IBM Victor V256, Multiprocessor for Program Mapping and Network Reconfiguration, IEEE Transactions on Parallel and Distributed Systems, IEEE Service Center, vol. 5, no. 9, September 1, 1994, pages 977-994, XP000460490					September 1, 1994
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DAL01:896445.1

PTO-1449 MAY 08 2006 Information Disclosure Citation from Application		Application No. 10/826,959		Applicant(s) James D. Ballew et al.			
		Docket Number 064747.1015	Group Art Unit	Filing Date April 15, 2004			
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EM	A	2003/0154112	08/14/2003	Neiman et al.	705	5	10/29/2002
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EM	E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012643, 6 pages, 04/19/2006, and Written Opinion of the International Search Authority, 5 pages					Mailed 04/19/2006
	F	Haynes et al., "A Visualization Tool for Analyzing Cluster Performance Data," 42nd Annual Symposium on Foundations of Computer Science, (FOCS 2001), October 8, 2001, pages 295-302					October 14-17 2001
	G	Patel et al., "Sage: An Application Development Tool Suite for High Performance Computing Systems," Aerospace Conference Proceedings, 2000, IEEE, Vol 11, pages 491-500					March 18-25 2000
	H	Baraglia et al., "RsdEditor: A Graphical User Interface for Specifying Metacomputer Components," Heterogeneous Computing Workshop Proceedings, 9th Cancun, Mexico, pages 336-345					May 1, 2000
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	B	6,735,660	05/11/2004	Osten et al.				
	C	2003/0188071	10/02/2003	Kunjan et al.				
	D	2004/0024949	02/05/2004	Winkler et al.				
	E	2004/0268000	12/30/2004	Barker et al.				
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EM	H	Wong, William, "Switch-Chip Fuels Third-Generation InfiniBand," November 10, 2003, Electronic Design, 2 pages					11/10/2003	
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